

FIG. 1

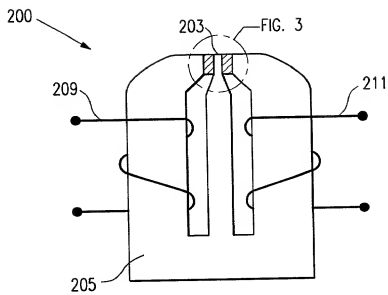


FIG. 2

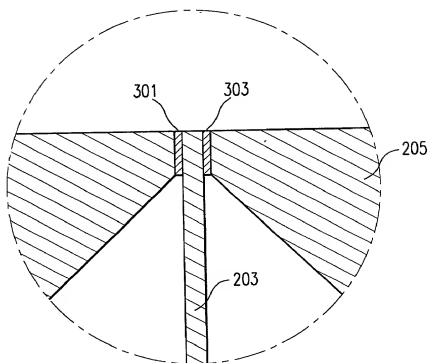


FIG. 3

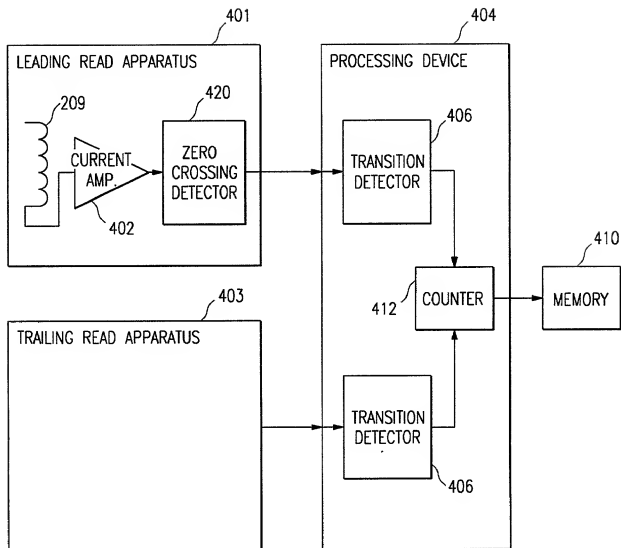


FIG. 4

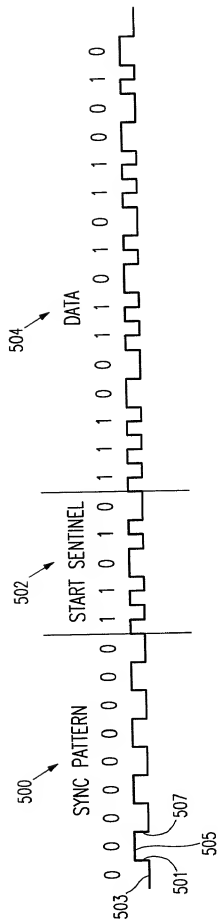


FIG. 5

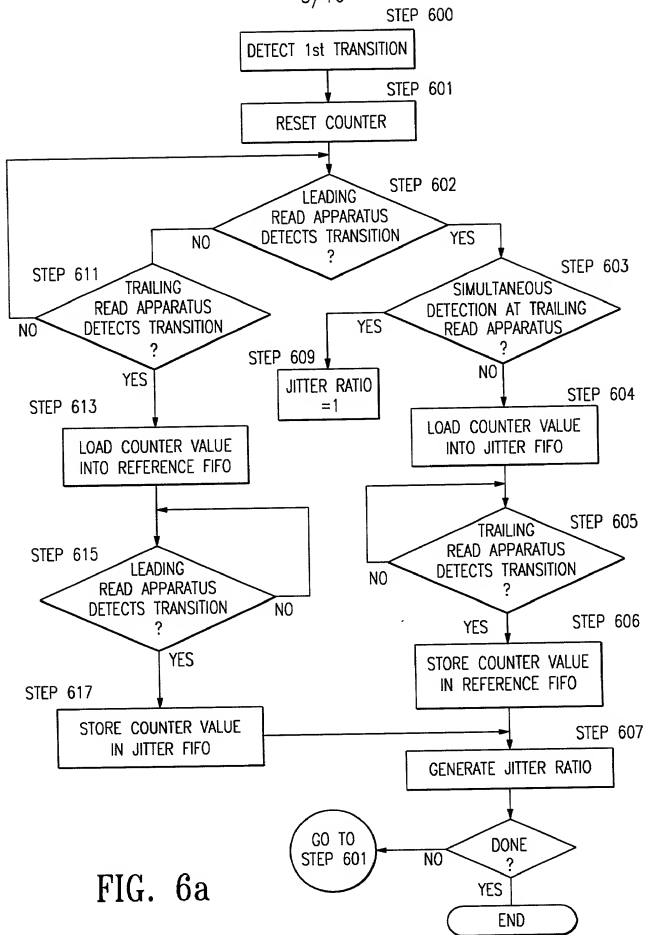


FIG. 6a

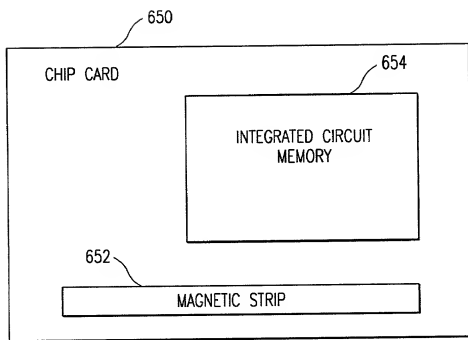


FIG. 6b

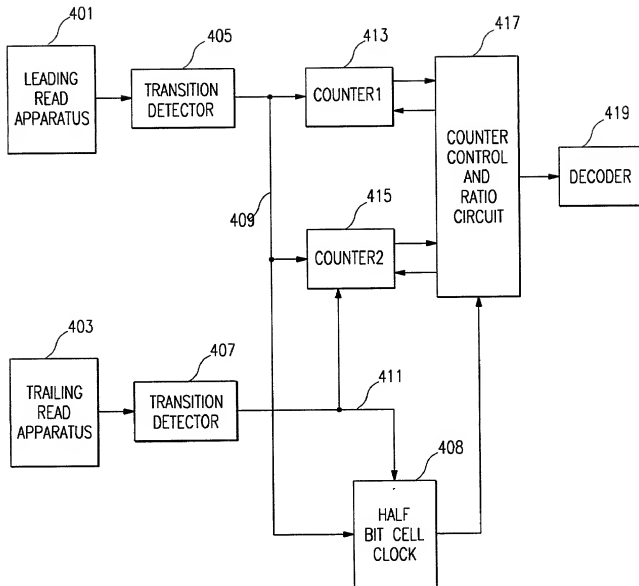


FIG. 7

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STEP 801

DETECT REFERENCE CELL  
AND SET COUNTER VALUE

STEP 803

START COUNTER

STEP 805

DETECT NEXT  
TRANSITION OF HALF  
BIT CELL CLOCK

NO

YES

STEP 807

INCREMENT HBCC

STEP 809

JCOUNTER  
RUNNING  
?

NO

YES

STEP 811

DETECT NEXT  
TRANSITION  
AT JCOUNTER  
?

NO

YES

STEP 813

STOP JCOUNTER

STEP 815

HBCC  
 $\pm$  REF HBCC +  
N HALF  
BIT CELLS  
?

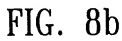
NO

YES

A

FIG. 8a

00000000-070001



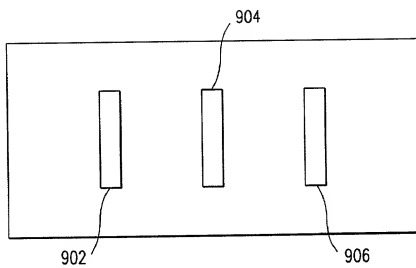


FIG. 9

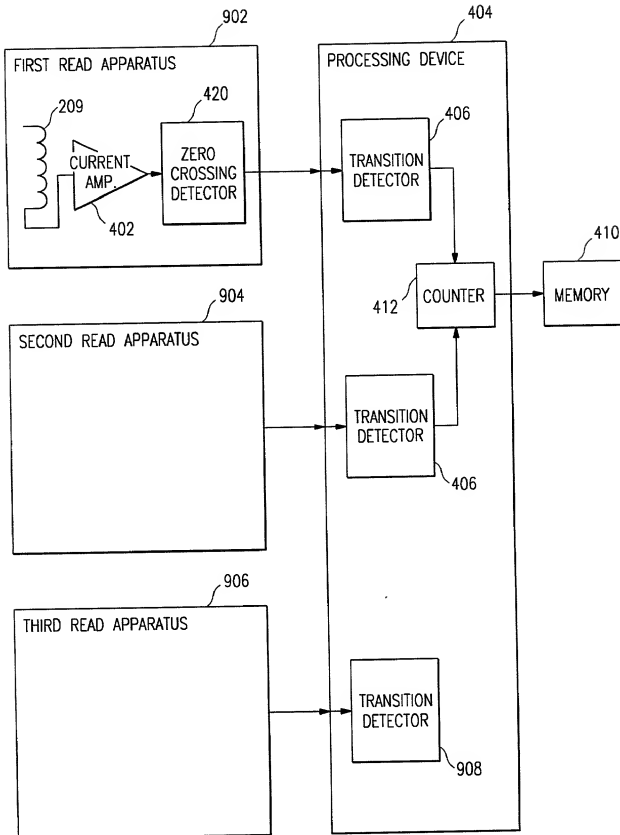


FIG. 10

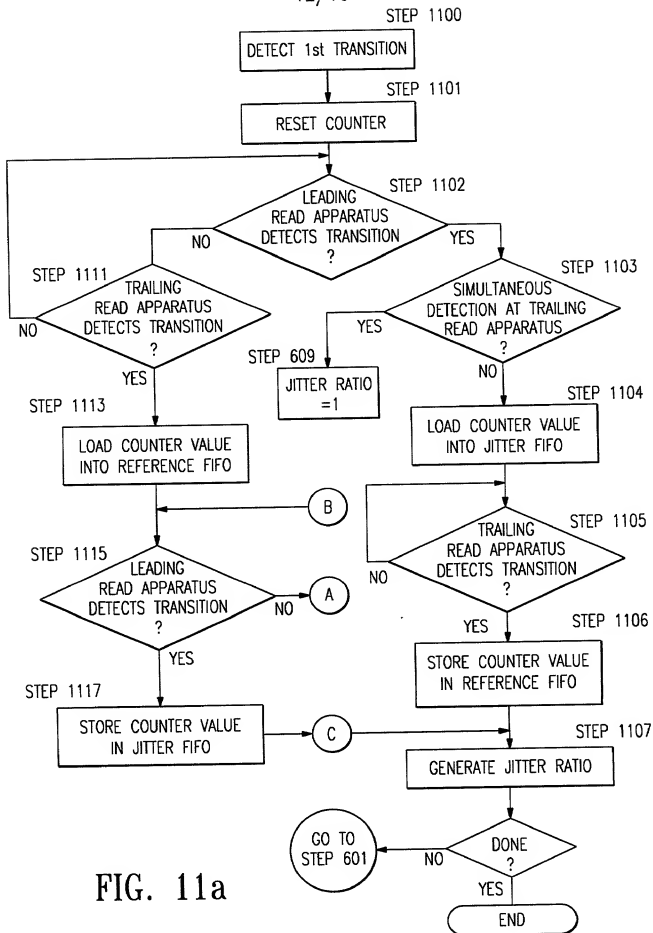


FIG. 11a

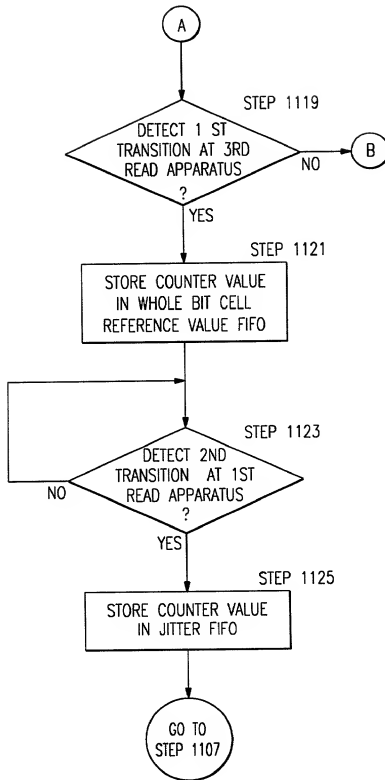


FIG. 11b

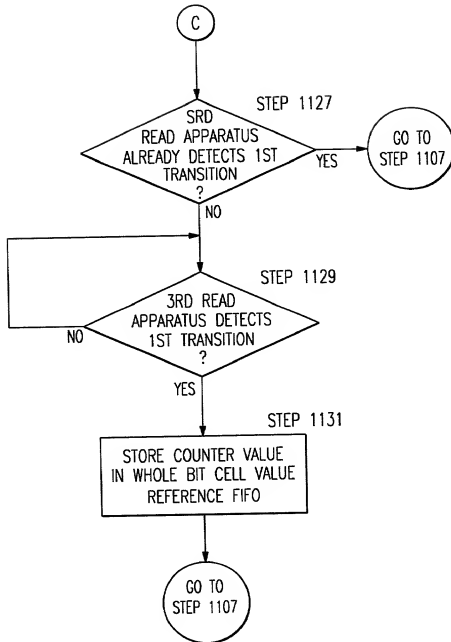


FIG. 11c

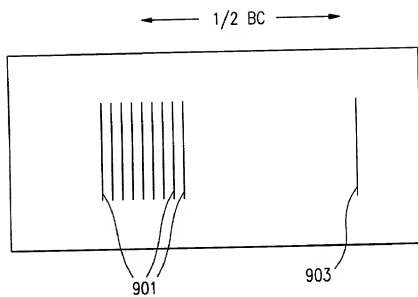


FIG. 12

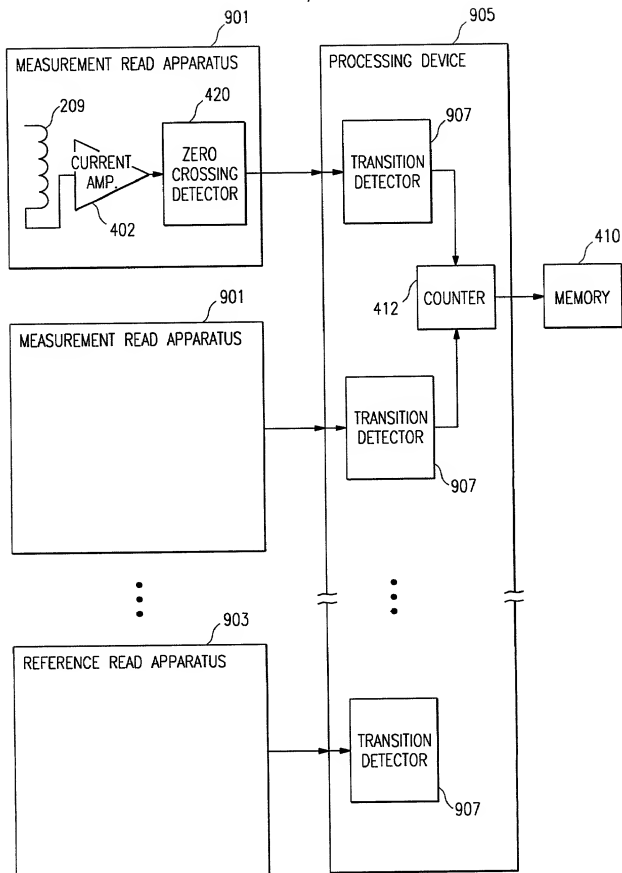


FIG. 13